



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al
Serial No. : 08/690,747
Filed : August 1, 1996

Art Unit : 1765
Examiner : Robert Kunemund
Confirmation No.: 1369
Notice of Allowance Date: May 11, 2004

Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

MAIL STOP PGPUB-DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

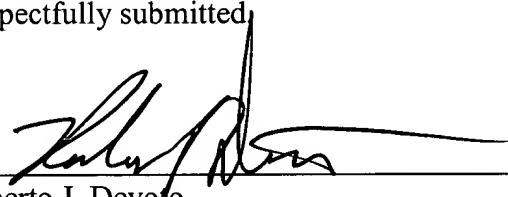
TRANSMITTAL OF FORMAL DRAWINGS

In response to the Notice of Allowability mailed May 11, 2004, please substitute the enclosed 10 sheets of Formal Drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 8/10/04


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